

FIGURE 1

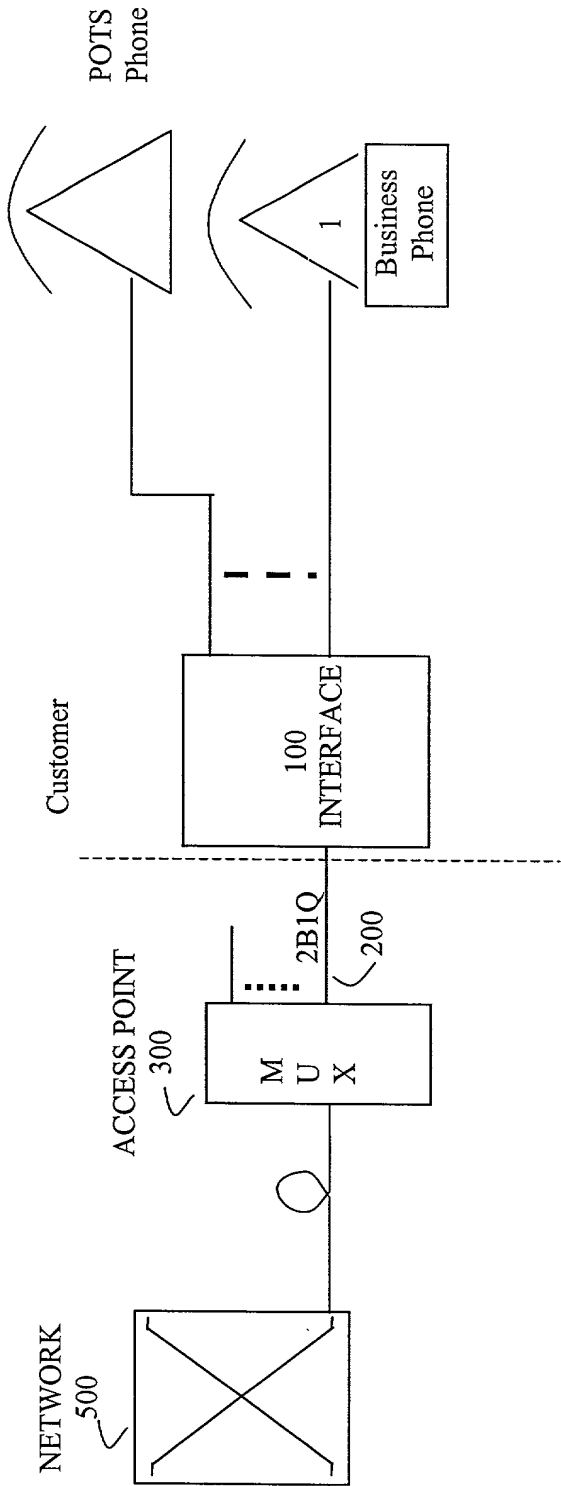


FIGURE 6

LSB byte n	LSB byte n+8	LSB byte n+16	LSB byte n+24	LSB byte n+32	LSB byte n+40	LSB byte n+48	LSB byte n+56
F	S	S	S	S	S	S	S

SIGNALING BYTE Carried on Ch 5

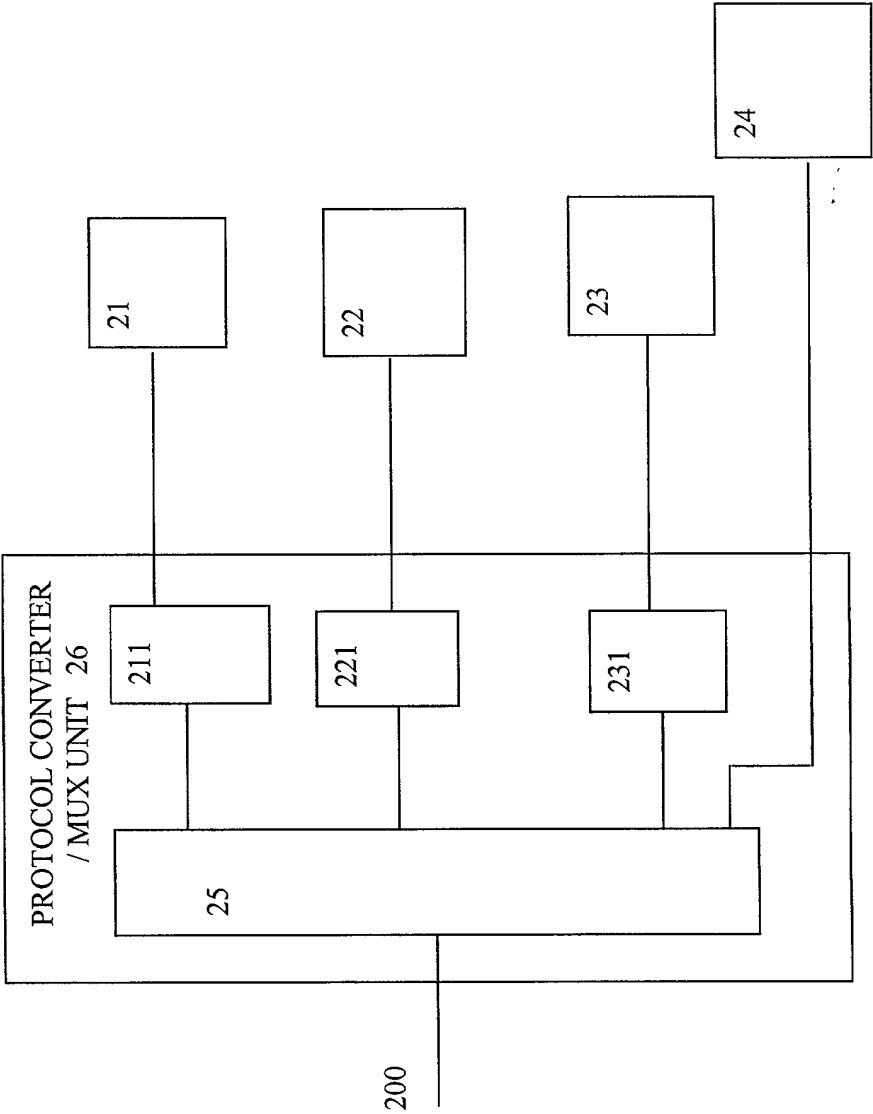


FIGURE 2

FIGURE 2

3/6

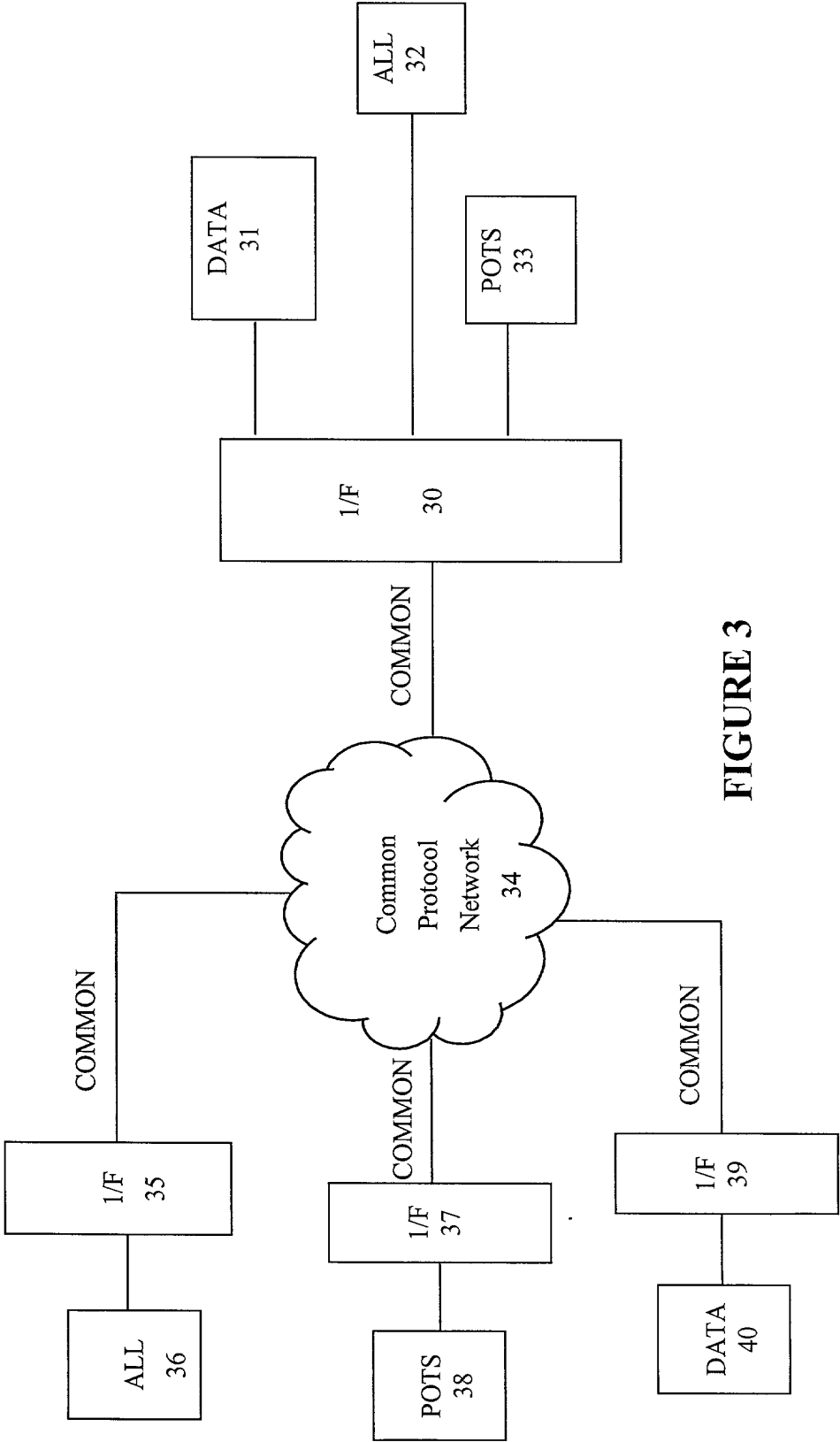


FIGURE 3

FIGURE 3

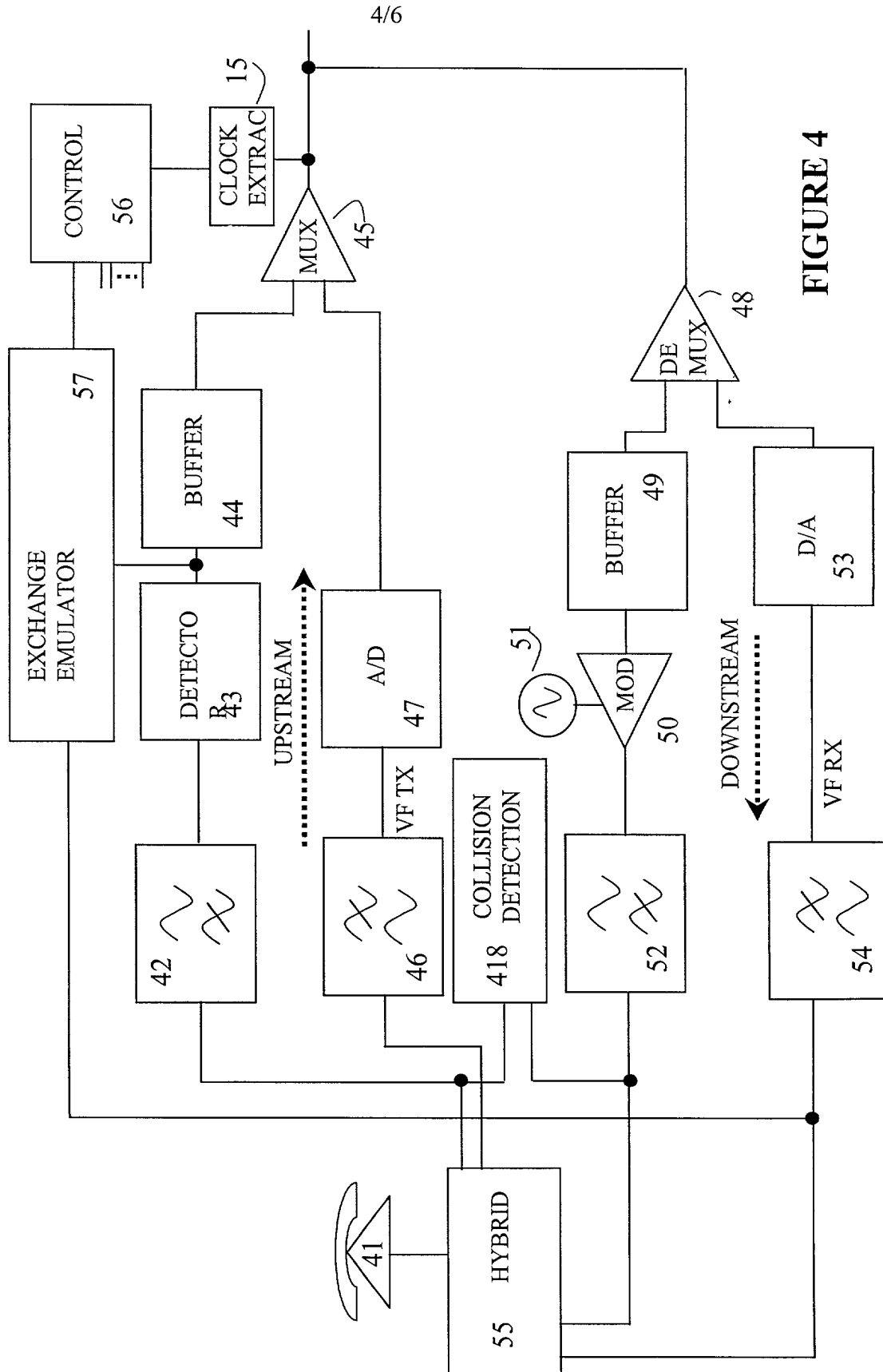


FIGURE 4

FIGURE 4

5/6

FIGURE 5

8 Ch @ 8 Bit/Byte, 64kb/s	Ch 1	Ch 2	Ch 3	Ch 4	Ch 5	Ch 6	Ch 7	Ch 8
Ch5 Byte "n" LSB Robbing	D	D	D	D	D	D	D	F
Ch5 Byte n+1 No Robbing	D	D	D	D	D	D	D	D
Ch5 Byte n+2 No Robbing	D	D	D	D	D	D	D	D
Ch5 Byte n+3 No Robbing	D	D	D	D	D	D	D	D
Ch5 Byte n+4 No Robbing	D	D	D	D	D	D	D	D
Ch5 Byte n+5 No Robbing	D	D	D	D	D	D	D	D
Ch5 Byte n+6 No Robbing	D	D	D	D	D	D	D	D
Ch5 Byte n+7 No Robbing	D	D	D	D	D	D	D	D
Ch5 Byte n+8 LSB Robbing	D	D	D	D	D	D	D	S

6/6

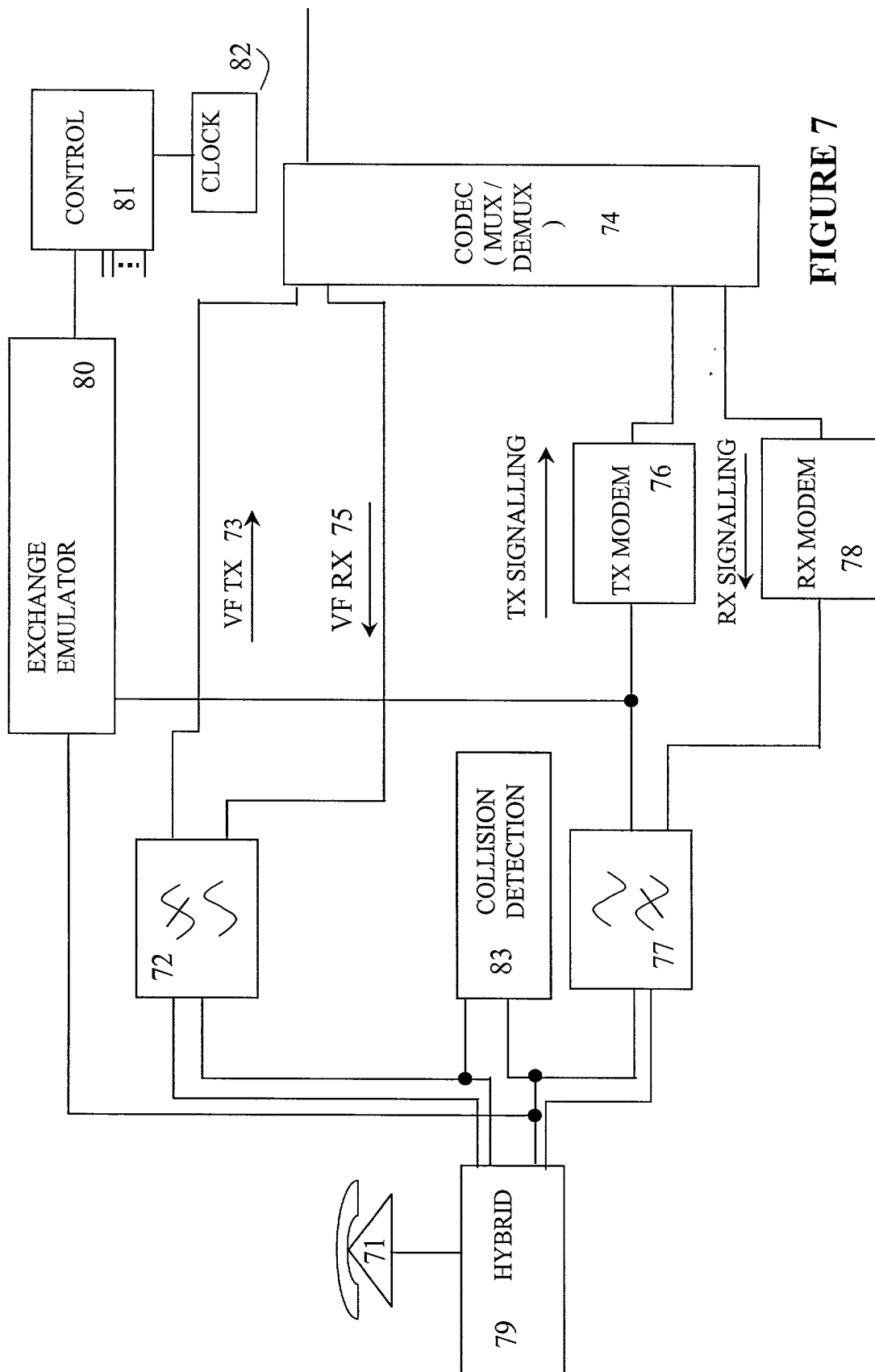


FIGURE 7

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